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# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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## **FACSIMILE COVER SHEET**

X FACSIMILE COVER SHEET  NEW APPLICATION DECLARATION (# Pages) ASSIGNMENT (# Pages) FORMAL DRAWINGS INFORMAL DRAWINGS CONTINUATION APP'N (# Pages) DIVISIONAL APP'N  NAME OF INVENTOR(S): Pentakota		X AMENDMENT 115 (7 Pages) EOT (X Month) NOTICE OF APPEAL APPEAL (# Pages) ISSUE FEE (# Pages) CHANGE IN CORRESPONENCE ADDRESS RECEIPT DATE & SERIAL NO.:
TITLE OF INVENTION:  LOW NOISE OUTPUT BUTTIFILE NO.:  TI-37302  FAXED: [1-1-0-5]  DUE: 10/5/2005  ATTY/SEC'Y: WDS/Ne	UFFER DEPOSITACCT, NO.: 20-0668	Serial No.: 10/775,022 Filling Date: 2/9/2004

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Texas Instruments Incorporated PO Box 655474, M/S 3999 Dallas, TX 75265

# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Pentakota

Docket No:

TI-37302

Serial No:

10/775,022

Examiner:

Wells, Kenneth

Filed:

2/9/2004

Art Unit:

2816

For:

OUTPUT BUFFER FOR DITITAL LOCK SIGNAL USING AN ANALOG

AMPLIFIER, A CONVERTER CIRCUIT, AND A BIAS CIRCUIT

## AMENDMENT UNDER 37 C.F.R. § 1.116

Assistant Commissioner For Patents Washington, DC 20231

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Foliamie Chambers

Dear Sir.

Responsive to the Office Action mailed August 5, 2005 in connection with the above identified application, Applicants respectfully submit the following amendments and remarks.

### IN THE TITLE

Please cancel the current title and substitute as the following from the new title.

OUTPUT BUFFER FOR DITITAL LOCK SIGNAL USING AN ANALOG AMPLIFIER,

A CONVERTER CIRCUIT, AND A BIAS CIRCUIT